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Address signal A13Address signal A15. PAD2 status. PAD1 status. Data set ready signal. Key common signal KC4. Write enable signal of RAM WR. Address signal of ROM Bank 0. Latch enable for Address decoder ASTB. Key input signal KI12. Key input signal KI13. Address signal of ROM Bank2. Address signal of ROM Bank 1. Motor drive signal of printer MD. Drawer drive signal DRW. Buzzer signal BUZZ. Paper feed signal of printer FD. Head drive signal of printer HD.A. Head drive signal of printer HD.B. Head drive signal of printer HD.C. Head drive signal of printer HD.DOutOut. Out. In. OutOutOut. Status of Status of. Power On Power OFFHigh. High. HighLowHigh. Low. Pulse. HighHigh. Pin No. SignalHead drive signal of printer HD.E. Head drive signal of printer HD.F. Head drive signal of printer HD.G. Head drive signal of printer HD.H. Reset signal RESET. Dot pulse form printer DP. Reset pulse from printer RP. Mode key status OFF. Power down signal PWD. Address signal for RAM Bank 0. Address signal for RAM Bank 1. Address signal for RAM Bank 2System clock 5 MHzSub system clock 32.768 KHzVPP detection signal VPP.S. Address signal of Fiscal ROM bank 0. Address signal of Fiscal ROM bank 1. Address signal of Fiscal ROM bank 2Out. OutInInIn. Power On Power OFF. HighPulse. PulseHigh. HighPin No. SignalNot used GND. Not used GND. Data out signal to CPU. Data in signal from CPUData strobe signal STB. Not used. Drawer sensor signal DRW.SDisplay segment signal Sb. Display segment signal Sc. Display segment signal Sd. Display segment signal Se. Display segment signal Sf. Display segment signal Sg. Display segment signal Sdp. Display segment signal Str. Display segment common signal Scom. Power for display VN. Display digit signal G10. Display digit signal G9. Display digit signal G8. Display digit signal G7. Display digit signal G6. Display digit signal G5. Display digit signal G4. Display digit signal G3. Display digit signal G2. Display digit signal G1Not used. Not usedInInOut. OutOut.

OutStatus of Status of. HighHighLow. LowLow. LowPulseVDD Voltage of memory protection battery. To Pin No.60 of CPU. When the voltage level at Pin No.60 of CPU is not stabilized, CPU does not work properly in rare case. Therefore, this machine uses the initialize IC for stabilizing the voltage. Even the voltage level of VDD Pin No.2 is changed, Pin No.1 of initilize IC outputs stabilized 5 volts. When the VDD voltage become less than 1.9 V, the initialize IC send a reset signal to CPU.To Pin No.64 of CPU. When the VP voltage become less than 4.8 V, the pin No.1 of IC5 become "Low" level. Then,the transistor Q44 become OFF. When Q44 become OFF, the voltage lebel of pin No.64 of CPU changes to "High" level from "Low". Then. CPU knows power failure.To select the address, CPU use

the IC13. CPU send the address to IC13, and send ASTBThen, IC13 store the address and output theIn this way, CPU select the address and dataKI signal port F800,F801Internal RAMShort direct areaRAM BANK selectGeneral purposeSpecial functionNormally, the transistor of motor drive circuit is followng condition.VP level. When the CPU want to rotate the motor, CPU change the MD signal to "High" from "Low". Then, TR1, TR2 and TR5 are become ON and MD signal is become GND level,Head drive circuit. This signal goes to printer unit, and then print. RP Reset pulse from printer. DP Dot pulse from printerProtection circuit. To prevent the circuit breakage, the protection circuit watches the voltage level of VCC, VDD and VPP. When the each voltage level is more than following condition, this circuit cuts the voltage.Detection voltage level Unit V. Please do not offer the downloaded file for sell only use it for personal usage. Looking for other manual For this no need registration. May be help you to repair. You could suffer a fatal electrical shock. Instead, contact your nearest service center. Note! To open downloaded files you need acrobat reader or similar pdf reader program.

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